

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	"6744968".PN.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 13:14
L2	1	"6774968".PN.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 13:19
L3	177671	(liquid ADJ1 crystal).AB. OR (liquid ADJ1 crystal).CLM.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 13:19
L4	7872	3 AND (pixel ADJ1 electrode).CLM.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 13:20
L5	5816	3 AND (pixel ADJ1 electrode).CLM. AND ((gate OR scan\$5) ADJ1 electrode)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 13:20
L6	2677	3 AND (pixel ADJ1 electrode).CLM. AND ((gate OR scan\$5) ADJ1 electrode).CLM.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 13:20
L7	1698	6 AND "349"/\$.CCLS.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 13:21
L8	51	(ic ADJ1 chip) AND 7	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 14:46
L9	0	(ic ADJ1 chip) NEAR3 (includ\$5 OR compris\$5 OR contain\$5) NEAR3 (frame ADJ1 memory)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 14:47
L10	4	(ic ADJ1 chip) NEAR5 (frame ADJ1 memory)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 14:47
S1	1	"20050140700".PN.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 11:38
S2	1	"6061039".PN.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 12:00
S3	1	(global NEAR1 address\$5 NEAR1 display)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 11:39
S4	2	(global\$5 NEAR1 address\$5 NEAR1 display)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 11:39
S5	3	(global\$5 NEAR1 address\$5 NEAR1 matrix)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 11:40

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S6	10406	(address\$5 NEAR1 pixel)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 11:41
S7	3406	(stor\$5 NEAR5 address) AND S6	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 11:41
S8	1258	S7 AND "345"/\$.CCLS.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 11:41
S9	1383	(pixel WITH includ\$5 WITH (memory OR ram OR rom) WITH address)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 11:42
S10	674	S9 AND "345"/\$.CCLS.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 11:42
S11	53	(pixel NEAR3 includ\$5 NEAR3 (memory OR ram OR rom) NEAR3 address)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 11:42
S12	674	S10 AND "345"/\$.CCLS.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 11:42
S13	25	S11 AND "345"/\$.CCLS.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 11:42
S14	5	"19950839"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 14:47
S15	6458	(driver OR ic OR (driving ADJ1 means)) WITH (resolution)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 14:48
S16	465	(driver OR ic OR (driving ADJ1 means)) WITH ((adjust\$5 OR chang\$5 OR control\$5) NEAR3 (resolution))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 14:49
S17	115	S16 AND "345"/\$.CCLS.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 14:50
S18	7991	(plural\$5 ADJ1 (driver OR ic OR (driving ADJ1 means)))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 14:50
S19	3	S17 AND S18	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 14:54
S20	30943	(adjust\$5 OR chang\$5 OR control\$5) NEAR3 resolution	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 15:02

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S21	16	S18 AND S20 AND "345"/\$.CCLS.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 14:58
S22	36	(scaler ADJ1 chip)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 14:58
S23	16	(scaler ADJ1 chip) SAME resolution	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 14:58
S24	16	S23 AND display	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 14:58
S25	38675	(adjust\$5 OR chang\$5 OR control\$5 OR scal\$5) NEAR3 resolution	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 15:02
S26	8	S25 SAME (intermediate ADJ1 voltage)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 15:04
S27	0	S25 SAME (middle ADJ1 voltage)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 15:07
S28	1	"540219".PN.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/19 15:15
S29	1	"5402149".PN.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 09:59
S30	1	"20050140700".PN.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 10:04
S31	741	"349"/\$.CCLS. AND (ic ADJ1 chip)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 10:04
S32	75	"349"/\$.CCLS. AND (ic ADJ1 chip). CLM.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 10:05
S33	20029	(liquid ADJ1 crystal) NEAR3 between NEAR2 substrate	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 10:06
S34	10695	"349"/\$.CCLS. AND S33	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 10:06
S35	146	S34 AND chip.CLM.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 10:09

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S36	22	(remov\$5 NEAR3 (ic ADJ0 chip) NEAR3 wafer)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 10:11
S37	4	display AND S36	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 10:11
S38	19	(remov\$5 NEAR3 (ic ADJ0 chip) NEAR3 (substrate)) AND display	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/09/21 13:13